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SEMICONDUCTOR DEVICE

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[There are no amendments to this patent.]

Claim

Semiconductor device characterized in that it is made with two semiconductor chips connected, with there being a circuit provided, which selects operating signals and testing signals, as well as wiring that directs output from said selecting circuit to a second semiconductor chip via the section where both semiconductor chips are connected, and for the second semiconductor, with an AND gate being provided into which the output from said selecting circuit is input, as well as wiring that connects output from said AND gate to a sensing terminal on the first semiconductor chip, so that the connection state of the aforementioned connection section can be tested.

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